After power on, the initial relay output status of the PCL-725 is shown as below:

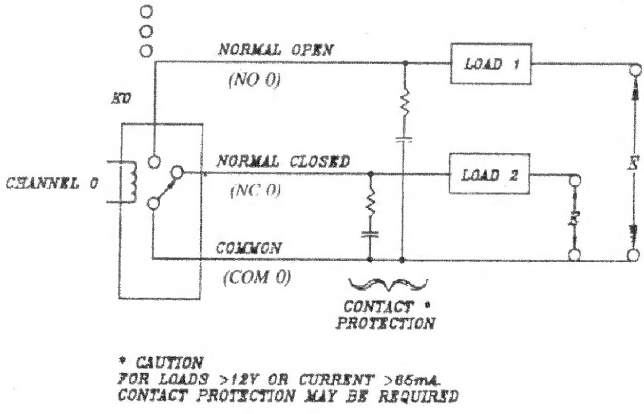


Fig. 5-1

A write operation to I/O address, BASE+0, will change the output status of each relay. For example, if Bit 0 of BASE+0 is set as "1" (logic high), relay 0, K0, will switch from position "NORMAL CLOSE", NC0, to position "NORMAL OPEN", NO0. This means that load2 will be de-energized, while load1 is energized.

To summarize, the "COMMON" line connect to the "NORMAL CLOSED" line, if the corresponding bit is set as 0 (power-on initial status). Otherwise, if the corresponding bit is set as 1, then the "COMMON" line will connect to the "NORMAL OPEN" line.

Warning: External circuits connected to the PCL-725's relay output port must be fused at 1A or lesss, and be rated at 120VA or less. Voltage must not exceed $\pm$120V peak.

For maximum relay contact life, and to prevent RFI if arcing occurs, contact protection is required for inductive loads greater than 12 V, @64mA.

For more information, please refer to section 5.1.